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26304 7590 02/22/2007

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**James J. Dade** (Depositor's name)  
 (Signature)  
**April 23, 2007** (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.           | CONFIRMATION NO. |
|-----------------|-------------|----------------------|-------------------------------|------------------|
| 10/625,337      | 07/22/2003  | Yasuyuki Oishi       | FUSA 15,617A<br>(100807-0008) | 8584             |

TITLE OF INVENTION: CORRELATOR AND DELAY LOCK LOOP CIRCUIT

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE DUE  | PUBLICATION FEE DUE | PREV. PAID ISSUE FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|----------------|---------------------|----------------------|------------------|------------|
| nonprovisional | NO           | \$1400         | \$300               | \$0                  | \$1700           | 05/22/2007 |
| EXAMINER       | ART UNIT     | CLASS-SUBCLASS |                     |                      |                  |            |
| TRAN, KHAI     | 2611         | 375-150000     |                     |                      |                  |            |
|                |              | 01 FC:1501     | 1400.00 DA          |                      |                  |            |
|                |              | 02 FC:1504     | 300.00 DA           |                      |                  |            |
|                |              | 03 FC:1501     | 38.00 DA            |                      |                  |            |

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**Fujitsu Limited**

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

**Kawasaki, Japan**

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Date Client/Matter Number

April 23, 2007 100807-00087

From Attorney Number

James J. Dade 03290

Phone Fax

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Total number of pages, including cover letter: 3

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**Comments:**

U.S. Patent Application No.: 10/625,337

**CORRELATOR AND DELAY LOCK LOOP CIRCUIT**

Our Reference No.: FUSA 15.617A (100807-00087)

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